# MRV328 V4.5.3.0 Update Notes

# <u>V4.5.3.0</u>

### 1. Fixed main line bugs

- Improved the driving capability and anti-interference capability of receiving card;
- 2) Fixed the problem that a row of pixels of the pre-stored image is missed after it is stored on receiving card

### 2. New features

Added support for SM16259 chip.

# **Earlier Version**

## <u>V4.5.0.0</u>

## 1. Supported driver chips

MBI series: MBI502x, MBI503x, MBI5041(B), MBI5042(B),

MBI5043, MBI505x, MBI5124 (excluding

MBI5124DPWM), MBI5125 (excluding

MBI5125DPWM), MBI515x, MBI5252;

SUM series: SUM20167, SUM2017(T), SUM2028, SUM203x,

SUM213x;

MY series: MY9266, MY9269, MY9366, MY9862, MY9868;

ICN series: ICN2027, ICN2028, ICN2038, ICN2038S, ICN2053; SM series: SM16158, SM16159, SM16207, SM16227, SM16237; Others: TLC5958, TLC59581, SCL8060, common chips.

#### 2. Supported decoding chips

- 1) 74HC138 chip;
- 2) 74HC595 chip;
- 3) RT5953 and RT5958 chips;
- 4) SM5266 chip;
- 5) ICN2012/2013 chips.

#### 3. Supported functions

- 1) Maximum loading capacity: 256×256 pixels;
- 2) Supports up to 16 sets of parallel data;
- Allows to package the FPGA and MCU programs together for update;
- 4) Supports monitoring function;
- 5) Supports setting of pre-stored image on receiving card;
- 6) Supports program readback;
- 7) Supports 5-pin LCD module.